Form A2-3: Compliance Verification Report for Type A Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99. The form can be used in a variety of ways as detailed below:

1. <u>To obtain Fully Type Tested status (≤ 50 kW)</u>

The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register. Tests 1 – 15 must all be completed and compliant for the **Power Generating Module** to be classified as **Fully Type Tested**.

2. To obtain Type Tested status for a product

This form can be used by the **Manufacturer** to obtain **Type Tested** status for a product which is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register.

Where the **Manufacturer** is seeking to obtain **Type Tested** status for an **Interface Protection** device the appropriate section of Form A2-4 should be used.

3. One-off Installation

This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99. This form shall be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the **Interface Protection** is to be demonstrated on site.

Note:

Within this Form A2-3 the term **Power Park Module** will be used but its meaning can be interpreted within Form A2-3 to mean **Power Park Module**, **Generating Unit or Inverter** as appropriate for the context. However, note that compliance shall be demonstrated at the **Power Park Module** level.

If the **Power Generating Module** is **Fully Type Tested** and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3-1 or A3-2) should include the **Manufacturer's** reference number (the system reference), and this form does not need to be submitted.

Where the **Power Generating Module** is not registered with the ENA Type Test Verification Report Register or is not **Fully Type Tested** this form (all or in parts as applicable) needs to be completed and provided to the **DNO**, to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99.

		Grid Tied Inverter With Storage System						
PGM tech	nology	X3-Hybrid-12.0-D X3-Hybrid-12.0-M X3-Fit-15.0-W	X3-Hybrid-15.0-D X3-Hybrid-15.0-M K3-Fit-15.0-M					
Manufacti	urer name	SolaX Power Netwo	ork Technology (Zhe jiang) Co., Ltd.					
Address		No. 288 Shizhu Road, Tonglu Economic Development Zone, Tonglu City, Zhejiang Province 310000, P.R. China						
Tel	+86(0571)-56260011	Web site	www.solaxpower.com					
E:mail	info@solaxpower.com							
			12kW					
Registered Capacity		15kW						
Energy storage capacity for Electricity Storage devices		11.5kWh, 17.3kWh, 23.0kWh, 34.6kWh, 46.1kWh; 6.1kWh, 9.2kWh, 12.3kWh						

There are four options for Testing: (1) **Fully Type Tested**(\leq 50 kW), (2) **Type Tested** product, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGM**s tests may be carried out at the time of commissioning (Form A4). **Type Tested** status is suitable for devices > 50 kW where the power quality aspects need consideration on a site by site basis in accordance with EREC G5 and EREC P28. Insert Document reference(s) for **Manufacturers' Information**

Tested option:	1. Fully Type Tested	2. Type Tested product	3. One-off Manufacturers' Info.	4. Tested on Site at time of Commissioning
0. Fully Type Tested - all tests detailed below completed and evidence attached to this submission		N/A	N/A	N/A
1. Operating Range	N/A	~		
2. PQ – Harmonics		\checkmark		
3. PQ – Voltage Fluctuation and Flicker		\checkmark		
4. PQ – DC Injection (Power Park Module s only)		\checkmark		
5. Power Factor (PF)		\checkmark		
6. Frequency protection trip and ride through tests		\checkmark		
7. Voltage protection trip and ride through tests		\checkmark		
8. Protection – Loss of Mains Test, Vector Shift and RoCoF Stability Test		~		
9. LFSM-O Test		\checkmark		
10. Protection – Reconnection Timer		~		
11. Fault Level Contribution		~		

There are four options for Testing: (1) **Fully Type Tested**(\leq 50 kW), (2) **Type Tested** product, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGM**s tests may be carried out at the time of commissioning (Form A4). **Type Tested** status is suitable for devices > 50 kW where the power quality aspects need consideration on a site by site basis in accordance with EREC G5 and EREC P28. Insert Document reference(s) for **Manufacturers' Information**

1. Fully Type Tested 2. Type Tested product **Tested option:** 3. One-off 4. Tested on Site at time of Manufacturers' Info. Commissioning 12. Self-monitoring Solid State Switch N/A 13. Wiring functional tests if required by para 15.2.1 N/A (attach relevant schedule of tests) 14. Logic Interface (input port) \checkmark 15. Cyber security N/A Manufacturer compliance declaration. - I certify that all products supplied by the company with the above Type Tested Manufacturer's reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site Modifications are required to ensure that the product meets all the requirements of EREC G99.

Signed	Xiao Yongli	On behalf of	SolaX Power Network Technology (Zhe jiang) Co., Ltd.
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Note that testing can be done by the Manufacturer of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.

A2-3 Compliance Verification Report –Tests for Type A Inverter Connected Power Generating Modules – test record

1. Operating Range: Tests should be carried with the **Power Generating Module** operating at **Registered Capacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within \pm 5 % of the apparent power value set for the entire duration of each test sequence.

Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

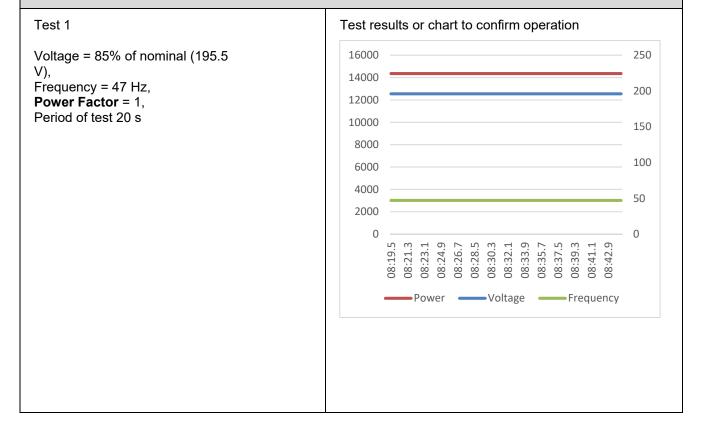
The Interface Protection shall be disabled during the tests.

In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source.

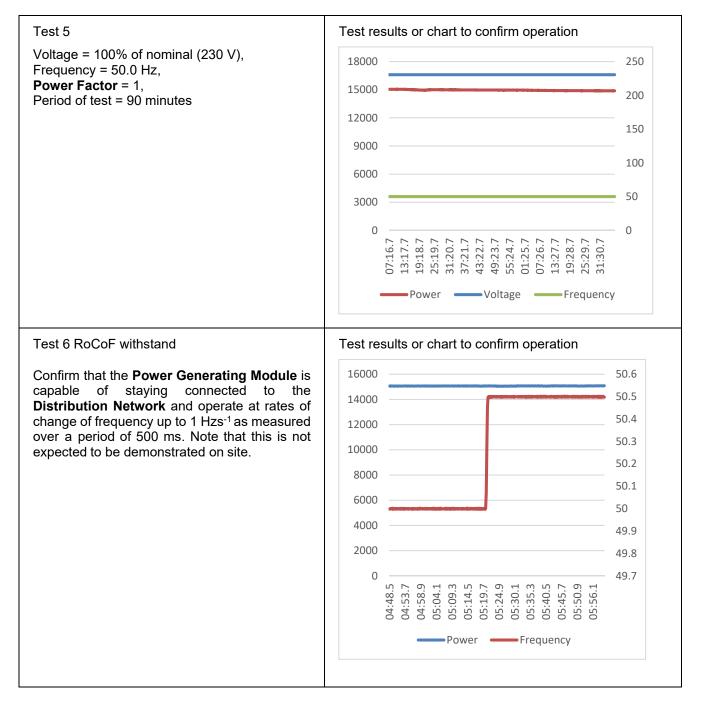
In case of a full converter **Power Park Module** (eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a DC source.

Pass or failure of the test should be indicated in the fields below (right hand side), for example with the statement "Pass", "No disconnection occurs", etc. Graphical evidence is preferred.

Note that the value of voltage stated in brackets assumes a **LV** connection. This should be adjusted for **HV** as required.



Test 2	Test results or chart to confirm operation					
Voltage = 85% of nominal (195.5 V), Frequency = 47.5 Hz, Power Factor = 1, Period of test 90 minutes	16000 15500 14500 14400 13500 13000 12500 12000 11500 11000 25:252 25:252 11000 25:470:2 26:470:2 11000 27:52:2 27:52:2 27:52:2 11000 26:470:2 27:52:2 11000 27:52:2 27:52:2 27:52:2 27:52:2 28:52:2 11000 27:52:2 27:50:2 27:50:2 27:50:2 27:50:2 27:50:2 27:50:2 27:50:2 27:50:2 28:2:2:2 29:47:3 20:50:2 20:50:2 20:50:2 20:2:2 20:2:2 20:2:2 20:2:2 20:2:2 20:2:2					
Test 3	Test results or chart to confirm operation					
Voltage = 110% of nominal (253 V), Frequency = 51.5 Hz, Power Factor = 1, Period of test 90 minutes	14000 2 12000 2 10000 2 8000 1 6000 1 4000 1	300 250 200 150 100 50				
Test 4	Test results or chart to confirm operation					
Voltage = 110% of nominal (253 V), Frequency = 52.0 Hz, Power Factor = 1, Period of test 15 minutes	14000 12000 10000 8000 6000 4000 2000	300 250 200 150 100 50 0				



2. Power Quality – Harmonics:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12, and measurements for the $2^{nd} - 13^{th}$ harmonics should be provided. The results need to comply with the limits of Table 2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment. For three phase **Power Generating Modules**, measurements for all phases should be provided.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC G5.

The rating of the **Power Generating Module** (per phase) should be provided below, and the Total Harmonic Distortion (THD) and Partial Weighted Harmonic Distortion (PWHD) should be provided at the bottom of this section.

Power Ge	nerating M	odule tested	d to BS EN	I 61000-3- 1	2			
Power G	enerating phase	Module ratir (rpp)	ng per	5		kVA	Value (A)	x % = Measured x 23/rating per ase (kVA)
(for single	e phase me	e measurem asurements umns below)	, only					
Harmonic		At 4	5-55% of I	Registered	l Capacit	y		
	Measur	ed Value (M Amps	V) in	Mea	isured Va	lue (MV) in %	Limit in	BS EN 61000-3- 12
	L1	L2	L3	L1	L2	L3	1 phase	3 phase
2	0.087	0.090	0.056	0.400	0.414	0.259	8%	8%
3	0.178	0.156	0.148	0.819	0.716	0.679	21.6%	Not stated
4	0.011	0.013	0.013	0.050	0.061	0.062	4%	4%
5	0.231	0.252	0.236	1.064	1.158	1.087	10.7%	10.7%
6	0.012	0.008	0.011	0.056	0.039	0.052	2.67%	2.67%
7	0.168	0.157	0.168	0.771	0.721	0.771	7.2%	7.2%
8	0.011	0.009	0.009	0.052	0.043	0.043	2%	2%
9	0.109	0.130	0.117	0.499	0.600	0.539	3.8%	Not stated
10	0.012	0.007	0.008	0.055	0.033	0.038	1.6%	1.6%
11	0.073	0.070	0.079	0.334	0.322	0.361	3.1%	3.1%
12	0.010	0.008	0.005	0.044	0.039	0.025	1.33%	1.33%
13	0.047	0.063	0.053	0.218	0.291	0.243	2%	2%
THD ²³				3.369	3.454	3.215	23%	13%

PWHD ²⁴				2.527	3.058	2.652	23%	22%	
Harmonic		At ⁻	100% of R	legistered	Capacity		Limit in	BS EN 61000-3-	
	Measure	d value (MV)	in Amps	Меа	asured value	e (MV) in %	12		
	L1	L2	L3	L1	L2	L3	1 phase	3 phase	
2	0.150	0.199	0.152	0.688	0.917	0.697	8%	8%	
3	0.220	0.251	0.288	1.011	1.154	1.323	21.6%	Not stated	
4	0.016	0.039	0.054	0.074	0.180	0.246	4%	4%	
5	0.581	0.584	0.583	2.674	2.685	2.684	10.7%	10.7%	
6	0.032	0.032	0.055	0.147	0.147	0.252	2.67%	2.67%	
7	0.483	0.463	0.479	2.221	2.132	2.203	7.2%	7.2%	
8	0.033	0.031	0.053	0.154	0.142	0.244	2%	2%	
9	0.334	0.337	0.339	1.536	1.550	1.561	3.8%	Not stated	
10	0.032	0.026	0.045	0.146	0.120	0.209	1.6%	1.6%	
11	0.192	0.199	0.184	0.881	0.917	0.847	3.1%	3.1%	
12	0.022	0.016	0.024	0.101	0.074	0.110	1.33%	1.33%	
13	0.086	0.068	0.075	0.394	0.313	0.344	2%	2%	
THD ²⁵				4.070	4.116	4.111	23%	13%	
PWHD ²⁶				2.041	2.266	2.418	23%	22%	
Power Generating Module rating per phase (rpp)					1.67 kVA			Harmonic % = Measured Value (A) x 23/rating per phase (kVA)	
Harmonic		Limit in	BS EN 61000-3-						
	Measur	ed Value (M Amps	V) in	Mea	sured Valu	12			
	L1	L2	L3	L1	L2	L3	1 phase	3 phase	
2	0.034	0.036	0.030	0.470	0.496	0.411	8%	8%	

							1		
3	0.042	0.037	0.031	0.582	0.506	0.431	21.6%	Not stated	
4	0.014	0.014	0.016	0.198	0.195	0.227	4%	4%	
5	0.035	0.035	0.036	0.480	0.486	0.503	10.7%	10.7%	
6	0.026	0.014	0.017	0.358	0.189	0.237	2.67%	2.67%	
7	0.020	0.013	0.013	0.276	0.184	0.185	7.2%	7.2%	
8	0.016	0.014	0.013	0.220	0.193	0.183	2%	2%	
9	0.016	0.013	0.014	0.223	0.182	0.193	3.8%	Not stated	
10	0.015	0.010	0.013	0.211	0.138	0.175	1.6%	1.6%	
11	0.026	0.033	0.015	0.354	0.451	0.211	3.1%	3.1%	
12	0.013	0.010	0.006	0.180	0.143	0.083	1.33%	1.33%	
13	0.007	0.012	0.010	0.102	0.165	0.139	2%	2%	
THD ²³				2.315	2.209	1.864	23%	13%	
PWHD ²⁴				6.236	6.565	5.701	23%	22%	
Harmonic		At 1	00% of R (egistered (Capacity				
	Measured	Value (MV)	in Amps	Меа	asured valu	Limit in BS EN 61000-3-12			
	L1	L2	L3	L1	L2	L3	1 phase	3 phase	
2	0.058	0.068	0.062	0.798	0.935	0.857	8%	8%	
3	0.059	0.043	0.058	0.813	0.591	0.798	21.6%	Not stated	
4	0.018	0.031	0.025	0.248	0.422	0.339	4%	4%	
5	0.068	0.030	0.048	0.933	0.417	0.663	10.7%	10.7%	
6	0.048	0.035	0.027	0.656	0.483	0.374	2.67%	2.67%	
7	0.079	0.054	0.064	1.087	0.751	0.881	7.2%	7.2%	
8	0.032	0.024	0.030	0.440	0.328	0.411	2%	2%	

9	0.034	0.015	0.026	0.474	0.208	0.354	3.8%	Not stated
10	0.015	0.020	0.013	0.204	0.273	0.175	1.6%	1.6%
11	0.042	0.048	0.020	0.574	0.662	0.280	3.1%	3.1%
12	0.027	0.025	0.011	0.374	0.346	0.149	1.33%	1.33%
13	0.073	0.078	0.061	1.008	1.071	0.839	2%	2%
THD ²⁵				2.384	2.113	1.952	23%	13%
PWHD ²⁶				5.184	5.361	4.991	23%	22%

3. Power Quality – Voltage fluctuations and Flicker:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC P28.

The standard test impedance is 0.4 Ω for a single phase **Power Generating Module** (and for a two phase unit in a three phase system) and 0.24 Ω for a three phase **Power Generating Module** (and for a two phase unit in a split phase system). Please ensure that both test and standard impedance are completed on this form. If the test impedance (or the measured impedance) is different to the standard impedance, it must be normalised to the standard impedance as follows (where the **Power Factor** of the generation output is 0.98 or above):

D max normalised value = (Standard impedance / Measured impedance) x Measured value.

Where the **Power Factor** of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the standard impedance.

The stopping test should be a trip from full load operation.

The duration of these tests needs to comply with the particular requirements set out in the testing notes for the technology under test.

The test date and location must be declared.

Test start date 11.06.2022			.2022		11.06.2022				
Test location No. 288 Shizhu Road, Tonglu Economic Devel Province 310000, P.R. China						opment Zone	e, Tonglu (City, Zhejiang	
		Starting)		Stopping				
	dmax	dc	d(t)	dmax	dc	d(t)	Pst	Plt 2 hours	

Measured Values at test impedance	0.179	0.6	89	0	0.285		0.803		D	0.096		0.093	
Normalised to standard impedance	NA	N	A	NA	NA		NA	NA		NA		NA	
Normalised to required maximum impedance	NA	N	A	NA	NA		NA	Ν	IA	A NA		NA	
Limits set under BS EN 61000- 3-11	4%	3.	3%	3.3%	4%		3.3%	3.	3.3% 1.0			0.65	
Test Impedance	R				Ω		XI				Ω		
Standard Impedance	R		0.24 * 0.4 ^		Ω		XI		0.15 * 0.25 ^			Ω	
Maximum Impedance	R				Ω		XI					Ω	
* Applies to t	hree phase	and s	split :	single phase	Power Ge	ene	rating Mod	lule s.	Delete	as appro	priat	e.	
^ Applies to s a three phase					Nodule and	d Po	ower Gene	erating	g Modu	ıle s using	two	phases on	
4. Power qua carried out at 217 A so DC	t three defin	ied p	ower	levels ±5%	At 230 V	a 50	0 kW three	phase	e Inver	ter has a	curr	ent output of	
The % DC in	jection ("as	% of	rated	d AC current	" below) is	cal	culated as	follows	s:				
% DC injection	on = Record	ed D	C va	lue in Amps	/ Base cur	rent	t						
where the ba than 0.25%.	ase current	is the	Reg	gistered Ca	pacity (W)) / V	/phase. The	e % D	C injec	ction shou	ld n	ot be greater	
					5K	ζ							
Test power le	evel			10%			559	%			100%		
_													

 Recorded DC value in Amps(L1/L2/L3)
 0.009/0.0125/0.015
 0.004/0.0021/0.0025
 0.0090/0.0140/0.0097

as % of rated AC current(L1/L2/L3)	0.124%/0.173%/0.207%	0.055%/0.029%/0.035%	0.124%/0.193%/0.134%							
Limit	0.25%	0.25%	0.25%							
15K										
Test power level	10%	55%	100%							
Recorded DC value in Amps(L1/L2/L3)	0.0017/0.0069/0.0082	0.0120/0.0230/0.0119	0.0132/0.0156/0.0157							
as % of rated AC current(L1/L2/L3)	0.008%/0.032%/0.038%	0.055%/0.106%/0.055%	0.061%/0.072%/0.072%							
Limit	0.25%	0.25%	0.25%							

5. Power Factor: The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity** and the measured **Power Factor** must be greater than 0.95 to pass. Voltage to be maintained within $\pm 1.5\%$ of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.

Note that the value of voltage stated in brackets assumes a LV connection. This should be adjusted for HV as required.

	5K											
Voltage			0.9	4 pu (216.2 \	/)	1 pu	(230 V)	1.	.1 pu (253 V)			
Measured	value			0.9998		0.9	996		0.9994			
Power Fa	ctor Limit			>0.95		>(0.95		>0.95			
15k												
Voltage			0.94	4 pu (216.2 V)	1 pu (230 V)	1.	1 pu (253 V)			
Measured	value			0.9999		0.9	999		0.9997			
Power Fa	ctor Limit			>0.95	>(0.95	>0.95					
	t ion – Freque r sts, frequency								Annex A.7.1.2.3. stated.			
Function	S	etting		rip test	p test		"No trip tests"					
	Frequency	Tin	ne delay	Frequency	Time	e delay	Frequen	cy /time	Confirm no trip			
U/F stage 1	47.5 Hz		20 s	47.49Hz	20	.06s	47.7 Hz 30 s		No trip			
U/F stage 2	47 Hz		0.5 s	47.05Hz	0.8	524s	47.2 Hz 19.5 s		No trip			
							46.8 0.4		No trip			

O/F	52 Hz	0.5 s	52.00Hz	0.526s	51.8 Hz 120.0 s	No trip
					52.2 Hz 0.45 s	No trip

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7. Protection – Voltage tests: These tests should be carried out in accordance with Annex A.7.1.2.2. For trip tests, voltage and time delay should be stated. For "no trip tests", "no trip" can be stated.

Note that the value of voltage stated below assumes a LV connection This should be adjusted for HV taking account of the VT ratio as required.

Function	Setting		Trip test		"No trip tests"		
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
U/V	0.8 pu (184 V)	2.5 s	183.5V	2.545s	188 V 5.0 s	No trip	
					180 V 2.45 s	No trip	
O/V stage 1	1.14 pu (262.2 V)	1.0 s	262.6V	1.048s	258.2 V 5.0 s	No trip	
O/V stage 2	1.19 pu (273.7 V)	0.5 s	274.1V	0.594s	269.7 V 0.95 s	No trip	
					277.7 V 0.45 s	No trip	

Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

8.Protection – Loss of Mains test: These tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4.

The following sub set of tests should be recorded in the following table.

Test Power and imbalance	33% -5% Q Test 22	66% -5% Q Test 12	100% -5% P Test 5	33% +5% Q Test 31	66% +5% Q Test 21	100% +5% P Test 10
Trip time. Limit is 0.5s ²⁷	0. 446s	0. 412s	0. 392s	0. 436s	0. 408s	0. 392s

Loss of Mains Protection, Vector Shift Stability test: This test should be carried out in accordance with Annex A.7.1.2.6. Confirmation is required that the **Power Generating Module** does not trip under positive / negative vector shift.

Start Frequency	Change	Confirm no trip
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Positive Vector Shift 49.5Hz		+50 degrees	No trip					
Negative Vector Shift	50.5Hz	- 50 degrees	No trip					
Loss of Mains Protection, RoCoF Stability test: This test should be carried out in accordance with Annex A.7.1.2.6. Confirmation is required that the Power Generating Module does not trip for the duration of the ramp up and ramp down test.								
Ramp range	Ramp range Test frequency ramp: Test Duration Confirm no trip							
49.0 Hz to 51.0 Hz	+0.95 Hzs ⁻¹	2.1 s	No trip					
51.0 Hz to 49.0 Hz -0.95 Hzs ⁻¹		2.1 s	No trip					

²³ If the device requires additional shut down time (beyond 0.5 s but less than 1 s) then this should be stated on this form. **9. Limited Frequency Sensitive Mode – Overfrequency test:** The test should be carried out using the specific threshold frequency of 50.4 Hz and **Droop** of 10%.

This test should be carried out in accordance with Annex A.7.1.3, which also contains the measurement tolerances.

Active Power response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex A.7.2.4.

Y/N

Alternatively, test results should be noted below:

,				
Test sequence at Registered Capacity >80%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00Hz ±0.01Hz	15014.5W	49.99Hz		-
Step b) 50.45Hz ±0.05Hz	15151.5W	50.45Hz		-
Step c) 50.70Hz ±0.10Hz	14276.0W	50.69Hz		-
Step d) 51.15Hz ±0.05Hz	12843.9W	51.14Hz		-
Step e) 50.70Hz ±0.10Hz	14179.6W	50.69Hz		-
Step f) 50.45Hz ±0.05Hz	15203.2W	50.44Hz		-
Step g) 50.00Hz ±0.01Hz	14971.7W	49.99Hz		
Test sequence at Registered Capacity 40% - 60%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00Hz ±0.01Hz	7462.5W	49.99Hz		-
Step b) 50.45Hz ±0.05Hz	7338.4W	50.44Hz		-
Step c) 50.70Hz ±0.10Hz	6589.8W	50.69Hz		-
Step d) 51.15Hz ±0.05Hz	5524.9W	51.14Hz		-
Step e) 50.70Hz ±0.10Hz	6587.1W	50.69Hz		-

Step f) 50.45⊦ ±0.05Hz	Iz 7	343.7w	50.44H	z			
Step g) 50.00Hz 72 ±0.01Hz		466.1W	49.99Hz				
10. Protection – F	Re-connectio	n timer					
and frequency to v should be provided	within the stag d in this form; ating Module	nection sequence si le 1 settings of Table both should be grea does not reconnect	e 10.1. Bot iter than 20	h the time) s to pass.	delay setting and th . Confirmation shou	ne measured delay Id be provided tha	
Time delay setting	Measured delay	Checks on no r			ltage or frequency i hits of Table 10.1.	s brought to just	
60s 61s		At 1.16 pu (266.2 V LV connection, 127.6 V HV connection assuming 110 V ph-ph VT)	2 At 0.78 pu (180.0 V LV connection, 85.8 V HV connection assuming 110 V ph-ph VT)		At 47.4 Hz	At 52.1 Hz	
Confirmation that the Power Generating Module does not re-connect.		No-Reconnection	No-Reconnection		No-Reconnection	No-Reconnection	
		These tests shall be even if the contribution				99 Annex A.7.1.5.	
		For	Inverter ou	utput			
Time aft	er fault	Volts			Amps		
20r	ns	29.4V			4.92A		
100ms		26.9V			0.09A		
250	ms						
500	ms						
Time to trip		0.36s	0.36s		In secon	n seconds	
12. Self-Monito	ring solid sta	ite switching: No sp	pecified tes	t requirem	ents. Refer to Anne	ex A.7.1.6.	
	dule, the volt	event of the solid sta tage on the output s 5 s.					
13. Wiring func	tional tests:	If required by para 1	5.2.1.				

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Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning)	NA
14. Logic interface (input port)	
Confirm that an input port is provided and can be used to shut down the module	Yes
Provide high level description of logic interface, e.g. details in 11.1.3.1 such as AC or DC signal (the additional comments box below can be used)	Yes
15. Cyber security	
Confirm that the Power Generating Module has been designed to comply with cyber security requirements, as detailed in 9.1.7.	NA
Additional comments.	