# Form A2-3: Compliance Verification Report for Type A Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99. The form can be used in a variety of ways as detailed below:

#### 1. To obtain Fully Type Tested status

The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register.

#### 2. To obtain Type Tested status for a product

This form can be used by the **Manufacturer** to obtain **Type Tested** status for a product which is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register.

Where the **Manufacturer** is seeking to obtain **Type Tested** status for an **Interface Protection** device the appropriate section of Form A2-4 should be used.

#### 3. One-off Installation

This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99. This form shall be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the **Interface Protection** is to be demonstrated on site.

Note:

Within this Form A2-3 the term **Power Park Module** will be used but its meaning can be interpreted within Form A2-3 to mean **Power Park Module**, **Generating Unit or Inverter** as appropriate for the context. However, note that compliance shall be demonstrated at the **Power Park Module** level.

If the **Power Generating Module** is **Fully Type Tested** and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3-1 or A3-2) should include the **Manufacturer's** reference number (the Product ID), and this form does not need to be submitted.

Where the **Power Generating Module** is not registered with the ENA Type Test Verification Report Register or is not **Fully Type Tested** this form (all or in parts as applicable) needs to be completed and provided to the **DNO**, to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99.

Manufac	turer's reference number	X1-AC-3.0 ; X1-AC-3.6				
PGM technology		Photovoltaic Grid-tied inverter				
Manufac	turer name	SolaX Power Network Technology (Zhe jiang) Co. , Ltd.				
Address		No.288 Shizhu Road, Tonglu Economic Development Zon Dongxing District, Tonglu City, Zhejiang Province, China.				
Tel	+86(0571)-56260011	Web site	www.solaxpower.com			
E:mail	info@solaxpower.com	·				
Registered Capacity			3.0kW			
Negister			3.68 kW			

There are four options for Testing: (1) **Fully Type Tested**, (2) Partially **Type Tested**, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGM**s tests may be carried out at the time of commissioning (Form A4).

Insert Document reference(s) for Manufacturers' Information

Tested option:	1. Fully Type Tested	2. Partially Type Tested	3. One-off Manufacturers'. Info.	4. Tested on Site at time of Commissioning
0. <b>Fully Type Tested</b> - all tests detailed below completed and evidence attached to this submission	~	N/A	N/A	N/A
1. Operating Range	N/A			
2. PQ – Harmonics				
3. PQ – Voltage Fluctuation and Flicker				
4. PQ – DC Injection (Power Park Modules only)				
5. Power Factor (PF)				
6. Frequency protection trip and ride through tests				
7. Voltage protection trip and ride through tests				
8. Protection – Loss of Mains Test, Vector Shift and RoCoF Stability Test				
9. LFSM-O Test				
10. Protection – Reconnection Timer				
11. Fault Level Contribution				

There are four options for Testing: (1) <b>Fully Type Tested</b> , (2) Partially <b>Type Tested</b> , (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of <b>Fully Type Tested PGMs</b> tests may be carried out at the time of commissioning (Form A4). Insert Document reference(s) for <b>Manufacturers' Information</b>										
Tested opt	ion:	1. F	Fully Type Tested	2. Partially Type Tested	3. One-off Manufacturers'. Info.	4. Tested on Site at time of Commissioning				
12. Self-mo	nitoring Solid State Switch									
13. Wiring functional tests if required by para 15.2.1 (attach relevant schedule of tests)		15.2.1								
14. Logic In	nterface (input port)									
manufactur	<b>rer</b> compliance declaration I ce ed and tested to ensure that they ets all the requirements of EREC	perform as stated ir								
Signed	Guo Huawei	On behalf of	SolaX Power Network Technology (Zhe jiang) Co. , Ltd.							
Note that testing can be done by the Manufacturer of an individual component or by an external test house.										
	Where parts of the testing are carried out by persons or organisations other than the <b>Manufacturer</b> then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.									

## A2-3 Compliance Verification Report –Tests for Type A Inverter Connected Power Generating Modules – test record

**1. Operating Range:** Two tests should be carried with the **Power Generating Module** operating at **Registered Capacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within  $\pm 5$  % of the apparent power value set for the entire duration of each test sequence.

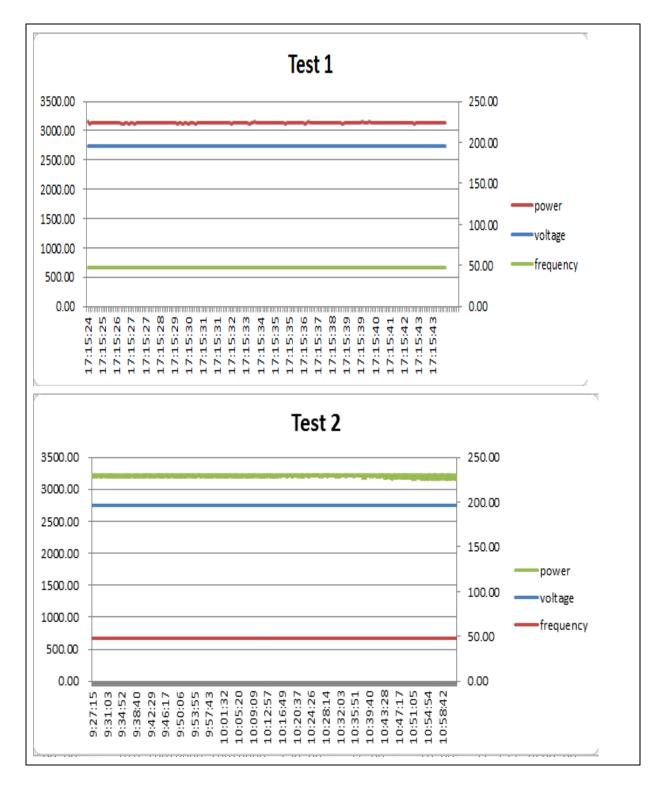
Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

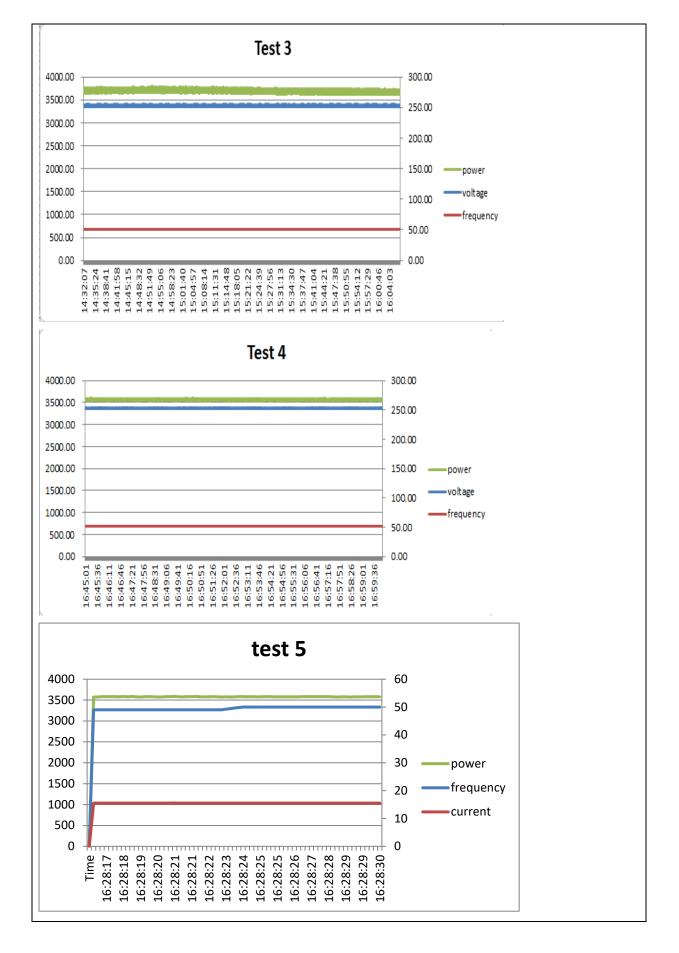
The Interface Protection shall be disabled during the tests.

In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source.

In case of a full converter **Power Park Module** (eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a DC source.

Test 1	
Voltage = 85% of nominal (195.5 V), Frequency = 47 Hz, <b>Power Factor</b> = 1, Period of test 20s	
Test 2 Voltage = 85% of nominal (195.5 V), Frequency = 47.5 Hz, <b>Power Factor</b> = 1, Period of test 90 minutes	
Test 3 Voltage = 110% of nominal (253 V)., Frequency = 51.5 Hz, <b>Power Factor</b> = 1, Period of test 90 minutes	
Test 4 Voltage = 110% of nominal (253 V), Frequency = 52.0 Hz, <b>Power Factor</b> = 1, Period of test 15 minutes	
Test 5 RoCoF withstand Confirm that the <b>Power Generating Module</b> is capable of staying connected to the <b>Distribution</b> <b>Network</b> and operate at rates of change of frequency up to 1 Hzs <sup>-1</sup> as measured over a period of 500 ms. Note that this is not expected to be demonstrated on site.	





### 2. Power Quality – Harmonics:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12The results need to comply with the limits of Table2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment.

**Power Generating Modules** with emissions close to the limits laid down in BS EN 61000-3-12 may require the installation of a transformer between 2 and 4 times the rating of the **Power Generating Module** in order to accept the connection to a **Distribution Network**.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC G5.

Power Generating Module tested to BS EN 61000-3-12

<b>Power Generating Module</b> rating per phase (rpp)		3.0	kVA	Harmonic % = $\mathbb{N}$ (A) x 23/rating p			
Harmonic At 45-55% of Capacity		Registered	100% of Regist Capacity	ered	Limit in BS EN 61000-3-12		
	Measured Value MV in Amps	%	Measured Value MV in Amps	%	1 phase	3 phase	
2	0.0125	0.0958	0.0148	0.1135	8%	8%	
3	0.0951	0.7291	0.1851	1.4191	21.6%	Not stated	
4	0.0025	0.0192	0.0030	0.0230	4%	4%	
5	0.0324	0.2484	0.0423	0.3243	10.7%	10.7%	
6	0.0040	0.0307	0.0029	0.0222	2.67%	2.67%	
7	0.0278	0.2131	0.0320	0.2453	7.2%	7.2%	
8	0.0023	0.0176	0.0042	0.0322	2%	2%	
9	0.0209	0.1602	0.0230	0.1763	3.8%	Not stated	
10	0.0029	0.0222	0.0029	0.0222	1.6%	1.6%	
11	0.0139	0.1066	0.0161	0.1234	3.1%	3.1%	
12	0.0040	0.0307	0.0039	0.0299	1.33%	1.33%	
13	0.0128	0.0981	0.0141	0.1081	2%	2%	
THD1		1.596%		1.464%	23%	13%	
PWHD <sup>2</sup>		0.196%		0.192%	23%	22%	

1 THD = Total Harmonic Distortion

<sup>2</sup> PWHD=Partial Weighted Harmonic Distortion

#### 2. Power Quality – Harmonics:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12The results need to comply with the limits of Table2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment.

**Power Generating Modules** with emissions close to the limits laid down in BS EN 61000-3-12 may require the installation of a transformer between 2 and 4 times the rating of the **Power Generating Module** in order to accept the connection to a **Distribution Network**.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC G5.

Power Generating Module tested to BS EN 61000-3-12

<b>Power Generating Module</b> rating per phase (rpp)			3.68	kVA	Harmonic % = N (A) x 23/rating p				
Harmonic	hic At 45-55% of <b>Registered</b> Capacity		100% of <b>Regist</b> Capacity	ered	Limit in BS EN 6	Limit in BS EN 61000-3-12			
	Measured Value MV in Amps	%	Measured Value MV in Amps	%	1 phase	3 phase			
2	0.0138	0.0863	0.0136	0.0850	8%	8%			
3	0.1092	0.6825	0.2285	1.4281	21.6%	Not stated			
4	0.0035	0.0219	0.0033	0.0206	4%	4%			
5	0.0345	0.2156	0.0471	0.2944	10.7%	10.7%			
6	0.0024	0.0150	0.0033	0.0206	2.67%	2.67%			
7	0.0301	0.1881	0.0342	0.2138	7.2%	7.2%			
8	0.0036	0.0225	0.0039	0.0244	2%	2%			
9	0.0201	0.1256	0.0248	0.1550	3.8%	Not stated			
10	0.0032	0.0200	0.0037	0.0231	1.6%	1.6%			
11	0.0146	0.0913	0.0188	0.1175	3.1%	3.1%			
12	0.0027	0.0169	0.0051	0.0319	1.33%	1.33%			
13	0.0126	0.0788	0.0176	0.1100	2%	2%			
THD <sup>1</sup>		1.460%		1.545%	23%	13%			
PWHD <sup>2</sup>		0.181%		0.258%	23%	22%			

3. Power Quality – Voltage fluctuations and Flicker:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable

Maximum Impedance.										
For <b>Power Generating Modules</b> of <b>Registered Capacity</b> of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC P28.										
	Starting			Stopping			Running			
	d max	dc	d(t)	d max	dc	d(t)	P st	P lt 2	hours	
Measured Values at test impedance	0.63%	0.04%	0%	0.1%	0.09%	0%	0.29	0.10	6	
Normalised to standard impedance	0.63%	0.04%	0%	0.1%	0.09%	0%	0.29	0.10	0.16	
Normalised to required maximum impedance	0.63%	0.04%	0%	0.1%	0.09%	0%	0.29	0.10	6	
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3% 3.3%		0	.65	
Test Impedance	)	R	0.4		Ω	XI	XI 0.25		Ω	
Standard Impedance		R	0.24 * 0.4 ^			XI	0.15 * 0.25 ^		Ω	
Maximum Impe	dance	R	0.4		Ω	XI	0.25		Ω	

\* Applies to three phase and split single phase **Power Generating Modules**.

^ Applies to single phase **Power Generating Module** and **Power Generating Modules** using two phases on a three phase system

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the **Power Factor** of the generation output is 0.98 or above.

Normalised value = Measured value x reference source resistance/measured source resistance at test point

Single phase units reference source resistance is 0.4  $\Omega$ 

Two phase units in a three phase system reference source resistance is 0.4  $\Omega$ 

Two phase units in a split phase system reference source resistance is 0.24  $\Omega$ 

Three phase units reference source resistance is 0.24  $\Omega$ 

Where the **Power Factor** of the output is under 0.98 then the XI to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to comply with the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below

Test start date	2017-05-10		Test end date			2017-05-10				
Test location		Building 4, No. 518, Xinzhuan Road, Caohejing Songjiang High- Tech Park, Shanghai, P.R. China (201705)								
<b>4. Power quality – DC injection:</b> The tests should be carried out on a single <b>Generating Unit</b> . Tests are to be carried out at three defined power levels ±5%. At 230 V a 50 kW three phase <b>Inverter</b> has a current output of 217 A so DC limit is 543 mA. These tests should be undertaken in accordance with Annex A.7.1.4.4.										
		X1-AC-	·3.0							
Test power level		10%	55%	%	100%					
Recorded value in Am	ips	0.0255	0.0	0.0192		0.0198				
as % of rated AC curr	ent	0.20%	0.1	5%	0.15%					
Limit		0.25%	0.2	25%	0.25%					
<b>4.</b> Power quality – DC injection: The tests should be carried out on a single Generating Unit. Tests are to be carried out at three defined power levels $\pm 5\%$ . At 230 V a 50 kW three phase Inverter has a current output of 217 A so DC limit is 543 mA. These tests should be undertaken in accordance with Annex A.7.1.4.4.										

X1-AC-3.6									
Test power level	10%	55%	100%						
Recorded value in Amps	0.0208	0.0131	0.0354						
as % of rated AC current	0.13%	0.08%	0.22%						
Limit	0.25%	0.25%	0.25%						

**5.** Power Factor: The tests should be carried out on a single Power Generating Module. Tests are to be carried out at three voltage levels and at **Registered Capacity**. Voltage to be maintained within  $\pm 1.5\%$  of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.

X1-AC-3.0									
Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)						
Measured value	0.999	0.999	0.999						
Power Factor Limit	>0.95	>0.95	>0.95						

**5. Power Factor**: The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity**. Voltage to be maintained within  $\pm 1.5\%$  of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.

X1-AC-3.6							
Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)				

Measured value			0.999		0,999		0.999			
Power Factor Limit			>0.9	95		>0.95		>0.95	>0.95	
6. Protection – Frequency tests: These tests should be carried out in accordance with the Annex A.7.1.2.3.										
Function	Setting			Trip test			"No trip test	s"		
	Frequency	Time de	lay	Frequency	Time delay		Frequency /time		Confirm no trip	
U/F stage 1	47.5 Hz	20 s		47.49Hz 20.1s		6	47.7 Hz 30 s		no trip	
U/F stage 2	47 Hz	0.5 s		46.98Hz	0.568	3s	47.2 Hz 19.5 s		no trip	
							46.8 Hz 0.45 s		no trip	
O/F	52 Hz	0.5 s		52.01Hz	0.572	2s	51.8 Hz 120 s		no trip	
							52.2 Hz 0.45 s		no trip	

Note. For frequency trip tests the frequency required to trip is the setting  $\pm 0.1$  Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting  $\pm 0.2$  Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7. Protection – Voltage tests: These tests should be carried out in accordance with Annex A.7.1.2.2.

Function	Setting		Trip test		"No trip tests"		
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip	
U/V	0.8 pu (184 V)	2.5 s	184.0V	2.52s	188 V 5.0 s	no trip	
					180 V 2.45 s	no trip	
O/V stage 1	1.14 pu (262.2 V)	1.0 s	262.5V	1.03s	258.2 V 5.0 s	no trip	
O/V stage 2	1.19 pu (273.7 V)	0.5 s	273.9V	0.574s	269.7 V 0.95s	no trip	
					277.7 V 0.45 s	no trip	

Note for Voltage tests the Voltage required to trip is the setting  $\pm 3.45$  V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting  $\pm 4$  V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

**8. Protection – Loss of Mains test:** These tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4.

The following sub set of tests should be recorded in the following table.							
Test Power and imbalance 33% 66% 100% 33% 66% 100   Test 22 -5% Q -5% Q -5% P +5% Q +5% Q +5% Q +5% Q +5% Q +5% Q 100% <							
Trip time. Limit is 0.5s	0.448s	0.156s	0.308s	0.166s	0.149s	0.220s	

Loss of Mains Protection, Vector Shift Stability test. This test should be carried out in accordance with Annex A.7.1.2.6.

	Start Frequency	Change	Confirm no trip	
Positive Vector Shift	49.5 Hz	+50 degrees	no trip	
Negative Vector Shift	50.5 Hz	- 50 degrees	no trip	

Loss of Mains Protection, RoCoF Stability test: This test should be carried out in accordance with Annex A.7.1.2.6.

Ramp range	Test frequency ramp:	Test Duration	Confirm no trip
49.0 Hz to 51.0 Hz	+0.95 Hzs <sup>-1</sup>	2.1 s	no trip
51.0 Hz to 49.0 Hz	-0.95 Hzs <sup>-1</sup>	2.1 s	no trip

	iency Sensitive Mode frequency of 50.4 Hz a		<b>st:</b> The test should be ca	arried out using the				
This test should be carried out in accordance with Annex A.7.1.3.								
Active Power response to rising frequency/time plots are attached if frequency ripiection tests are undertaken in accordance with Annex A.7.2.4.								
Alternatively, simulation results should be noted below:								
Test sequence at <b>Registered</b> <b>Capacity</b> >80%	Measured <b>Active</b> <b>Power</b> Output	Frequency	Primary Power Source	Active Power Gradient				
Step a) 50.00Hz ±0.01Hz	3679.6	50.00		-				
Step b) 50.45Hz ±0.05Hz	3640.9	50.45		-9.51%				
Step c) 50.70Hz ±0.10Hz	3458.8	50.70		-10.0%				
Step d) 51.15Hz ±0.05Hz	3127.6	51.15		-10.0%				
Step e) 50.70Hz ±0.10Hz	3457.2	50.70		-9.93%				
Step f) 50.45Hz ±0.05Hz	3638.8	50.45		-9.02%				
Step g) 50.00Hz ±0.01Hz	3675.1	50.00		-				
Test sequence at <b>Registered</b> <b>Capacity</b> 40% - 60%	Measured <b>Active</b> <b>Power</b> Output	Frequency	Primary Power Source	Active Power Gradient				
Step a) 50.00Hz ±0.01Hz	1975.9	50.00		-				
Step b) 50.45Hz ±0.05Hz	1955.8	50.45		-9.81%				
Step c) 50.70Hz ±0.10Hz	1857.1	50.70		-9.98%				
Step d) 51.15Hz ±0.05Hz	1679.7	51.15		-10.0%				
Step e) 50.70Hz ±0.10Hz	1857.3	50.70		-9.99%				
Step f) 50.45Hz ±0.05Hz	1955.2	50.45		-9.82%				

Step g) 50.00 ±0.01Hz	)Hz		1975.3		50.00				-	
10. Protection – Re-connection timer.										
Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1.										
Time delay setting	Mea dela	isured y		Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of Table 10.1.						
30s	35s		At 1.16 pu (266.2 V)		At 0.78 pu (180.0 V)	At 47.4	At 47.4 Hz		At 52.1 Hz	
Confirmation that the <b>Power Generating</b> <b>Module</b> does not re- connect.		No-reconnec	ction	No-reconnection	n No-rec	No-reconnection		No-reconnection		
11. Fault lev	el co	ntributi	on: These test	ts shal	I be carried out in	accordanc	e with ERE	C G99 A	nnex A.7.1.5.	
For Inverter	outpu	ıt								
Time after fai	ult				Volts Amps					
20ms			155 V	155 V 28.6A						
100ms			0		0					
250ms			0		0					
500ms			0		0					
Time to trip			0.557 s		In seconds					
<b>12. Self-Monitoring solid state switching:</b> No specified test requirements. Refer to Annex A.7.1.7.								7.1.7.		
It has been verified that in the event of the solid state switching device failing to disconnect the <b>Power Park Module</b> , the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s.							NA			
13. Wiring functional tests: If required by para 15.2.1.										
Confirm that the relevant test schedule is attached (tests to be undertaken at time of NA commissioning)							NA			
14. Logic interface (input port).										
Confirm that an input port is provided and can be used to shut down the module.						Yes				
Additional co	mme	nts.								